| Discipline | COMPUTER ORGANIZATION | code: 17 |
| :--- | :--- | :--- |
| Specialty | Computer Systems and Technologies |  |
| ECTS credits: 7 | Form of assessment: exam | winter semester |
| Lecturer | Assoc. Prof. Yulka Petkova, PhD <br> Room 207-3 E <br> Phone: $+35952383 ~ 403$ <br> E-mail: yulka.petkova @tu-varna.bg |  |
| Department | Computer Science and Engineering |  |
| Faculty | Faculty of Computing and Automation |  |
| Learning objectives: <br> The discipline is devoted to the structural organization and functioning of modern computer systems. <br> Types of data and the standards for their presentation are considered. Algorithms of arithmetic with <br> fixed and floating point numbers are studied. Based on the principles of organization and <br> implementation of the computer, the logical structure and functioning of its main devices and systems <br> are considered. The attitude of individual structural elements and primary algorithms to various <br> programmatic problems is elucidated. |  |  |


| CONTENTS: |  |  |
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| Training Area | Hours <br> lectures | Hours <br> seminar <br> exercises |
| Data - logical, symbolic, numeric. Number representation - fixed and floating <br> point. Standards for number representation. Features. Accuracy. Machine <br> codes. Formal means for presenting logical structures and language of micro- <br> operations. | 3 | 3 |
| Arithmetic-logic devices for a fixed point numbers. Arithmetic-logic floating <br> point devices. | 7 | 7 |
| Storage devices and operations in them. RAM-memory - static and dynamic <br> organization. FIFO and LIFO structures. Associative memory - associative <br> operations. Applications. | 4 | 4 |
| Organization of the computation process. Command cycle. Machine command <br> system. Addressing methods. Registers. | 4 | 4 |
| Interruption. Organization of the interruption system. Organization of I/O <br> system. System bus. Exchange methods. | 4 | 4 |
| Organization of the storage system. Buffer memories, commands, and <br> management algorithms. Organization of virtual memory. | 4 | 4 |
| Principles of organization of control. Logical structures of micro-programming <br> control devices. | 4 | 4 |
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