

Discipline	LEARNING PRACTICE - 1 PART	code: 11	summer semester
Specialty	Computer Systems and Technologies		
ECTS credits: 1	Form of assessment: ongoing assessment		
Lecturer	Assoc. Prof. Zhivko Zhekov, PhD Room 211 E, 713 E Phone: +359 52 383 395 E-mail: zhivko_zhekov@tu-varna.bg		
Department	Industrial Automation		
Faculty	Faculty of Computing and Automation		
Learning objectives:			
The course introduces the students to:			
<ul style="list-style-type: none"> • CAD system Multisim xx - capabilities, working window, tools - purpose, setup; libraries and elements included therein; power supplies and measuring instruments. • Construction and study of diode arresters, R-C differentiation and integration circuits, sizing of circuits, investigation of rectangular impulse generators. • Design and research of combinational logic circuits. • Design and study of logical memory circuits. 			
CONTENTS:			
Training Area		Hours lectures	Hours seminar exercises
Introduction to CAD system Multisim xx.		0	6
Power supplies and meters incorporated in the simulation software.		0	6
Diode limiters. R-C differentiating and integrating chain. Generators of rectangular pulses.		0	6
Design and study of combinational logic circuits.		0	6
Design and study of sequential logic circuits.		0	6
TOTAL:		0	30